

WHAT IS CLAIMED IS:

1. An input circuit of a television tuner,
comprising:

5 a first input terminal to which a television signal
is input;

 a second input terminal to which an FM broadcasting
signal is input;

 an input tuning circuit tuned to the television
10 signal and the FM broadcasting signal,

 wherein the first input terminal is connected to the
input end of the input tuning circuit;

 first and second diodes serially connected between
the second input terminal and the input end of the input
15 tuning circuit and simultaneously switched on or off,

 wherein the first diode is input to the input end of
the input tuning circuit, and

 wherein the second diode is provided at the side of
the second input terminal;

20 an inductance element interposed between the second
input terminal and the second diode; and

 a first capacitance element that forms a low pass
filter together with the inductance element in a state
when the second diode is switched on and is connected
25 between the connection point of the first diode and the
second diode and a ground.

2. The input circuit of the television tuner

according to Claim 1, further comprising a third diode that is switched on or off simultaneously with first and second switches and is interposed between the inductance element and the second diode; and

5 a second capacitance element that forms a low pass filter together with the inductance element and the first capacitance element in a state when the second and third diodes are switched on and is connected between the connection point of the second diode and the third diode
10 and the ground.

3. The input circuit of the television tuner according to Claim 1, further comprising an FM trap circuit provided between the first input terminal and the
15 input end of the input tuning circuit for attenuating an FM broadcasting band; and

 a fourth diode connected between the connection point of the first diode and the second diode and the input end of the FM trap circuit,
20 wherein the fourth diode is switched on or off simultaneously with the first through third diodes.

4. The input circuit of the television tuner according to Claim 3, further comprising a transistor
25 whose emitter is grounded and whose collector is pulled up to a power supply voltage by a feed resistor,

 wherein the cathodes of the first, second, and fourth diodes are connected to each other,

wherein the anodes of the second and third diodes are connected to each other,

wherein a bias voltage lower than the power supply voltage is supplied to the anodes of the first through
5 fourth diodes,

wherein the cathodes of the first, second, and fourth diodes are connected to the collector of the transistor through a common first resistor,

wherein the cathode of the third diode is connected
10 to the collector of the transistor through a second resistor, and

wherein the transistor is switched on or off.